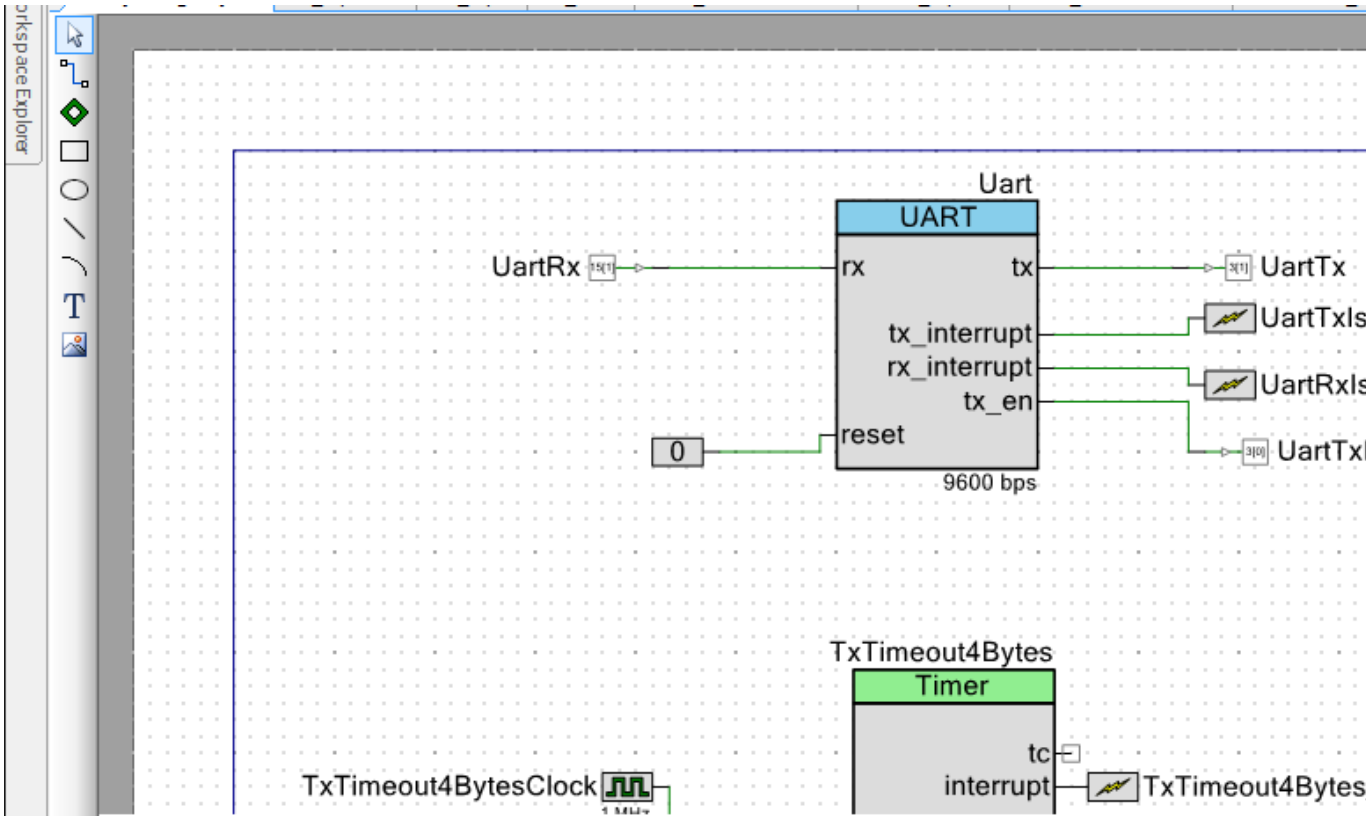
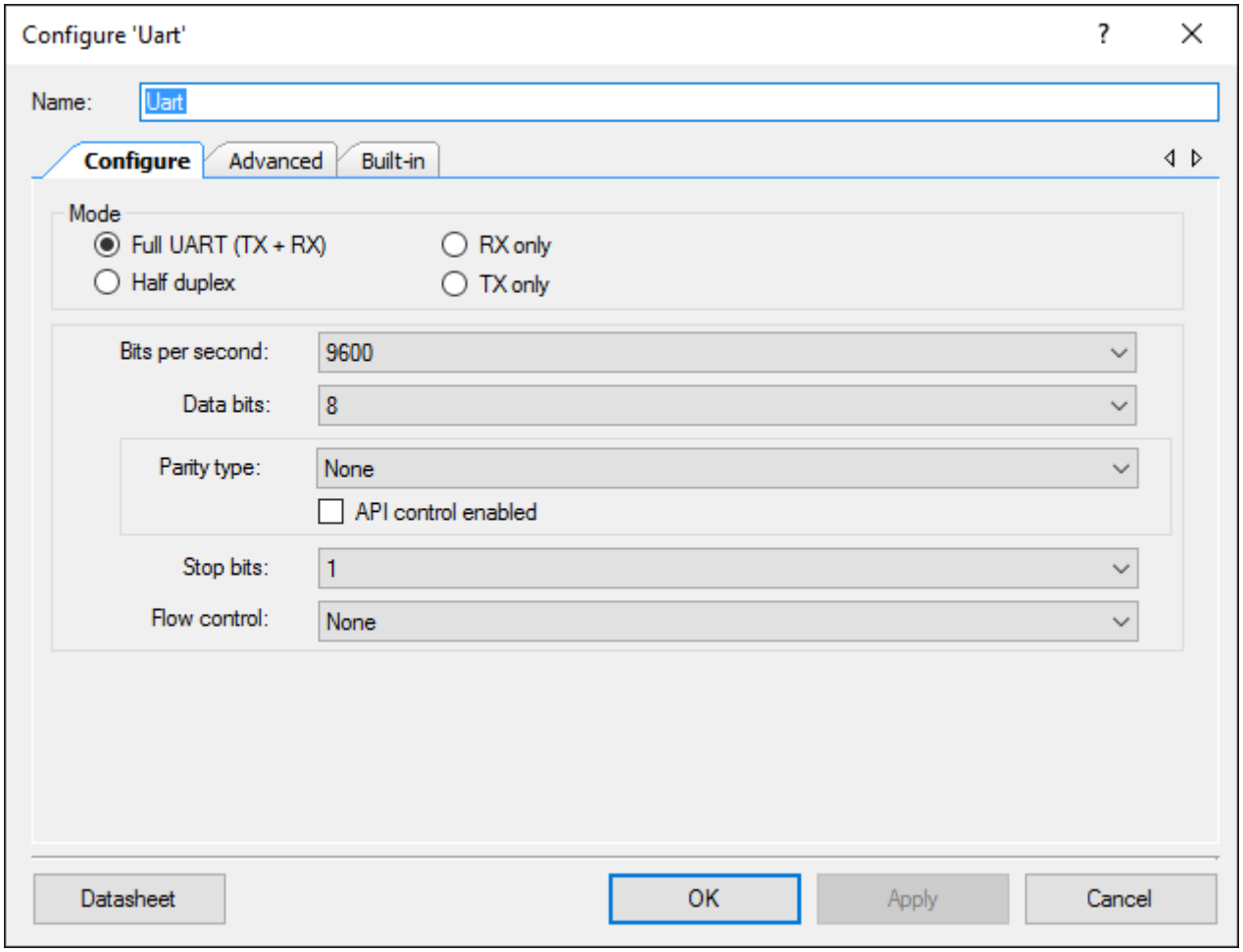
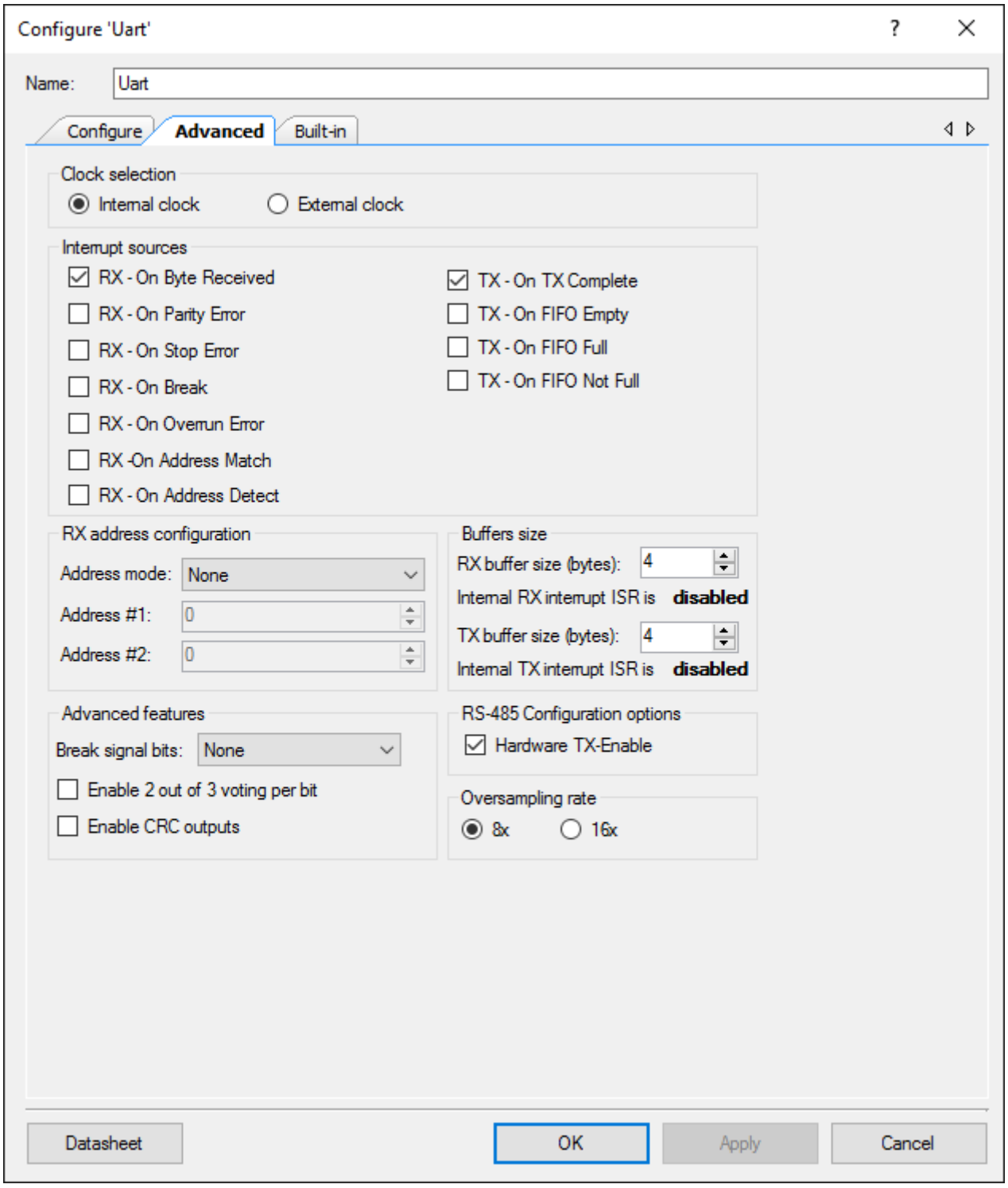
```
void BspIsr_UartRxIsr(void)
{
    /* Vaciar buffer Rx */
    WBsp_RxTimeout20msgStop();
    RxTimeout20msg_ISR_Stop();

    if (WBsp_UartRxBufferNotEmpty())
    {
      CommsData.ui8_RxBuffer[CommsData.ui8_RxBufferIdx] = WBsp_UartRxReadByte();
      /* Arrancar temporizador de timeout entre bytes recibidos */
      RxTimeout20msg_WriteCounter(COMMS_TIMEOUT_5SG);
      RxTimeout20msg_WritePeriod(COMMS_TIMEOUT_5SG);
      WBsp_RxTimeout20msgInit();
      WBsp_RxTimeout20msgEnable();
      CommsModbus.ui8_RxTimeout20msgIsr = 0;

      RxTimeout20msg_ISR_Start();

      /* Incrementar indice buffer Rx */
      CommsData.ui8_RxBufferIdx++;
    }
}

lllllllllllllllllllllllll
```


Uart
UART
UartRx
rx
tx
UartTx
tx_interrupt
UartTxIs
rx_interrupt
UartRxIs
tx_en
reset
0
9600 bps
TxTimeout4Bytes
Timer
tc
interrupt
TxTimeout4Bytes
TxTimeout4BytesClock


Configure 'Uart'
Name:
Uart
Configure
Advanced
Built-in
Mode
Full UART (TX + RX)
RX only
Half duplex
TX only
Bits per second:
9600
Data bits:
8
Parity type:
None
API control enabled
Stop bits:
1
Flow control:
None
Datasheet
OK
Apply
Cancel


Configure 'Uart'
Name:
Uart
Configure
Advanced
Built-in
Clock selection
Internal clock
External clock
Interrupt sources
RX - On Byte Received
RX - On Parity Error
RX - On Stop Error
RX - On Break
RX - On Overrun Error
RX -On Address Match
RX - On Address Detect
TX - On TX Complete
TX - On FIFO Empty
TX - On FIFO Full
TX - On FIFO Not Full
RX address configuration
Address mode:
None
Address #1:
0
Address #2:
0
Buffers size
RX buffer size (bytes):
4
Internal RX interrupt ISR is disabled
TX buffer size (bytes):
4
Internal TX interrupt ISR is disabled
Advanced features
Break signal bits:
None
Enable 2 out of 3 voting per bit
Enable CRC outputs
RS-485 Configuration options
Hardware TX-Enable
Oversampling rate
8x
16x
Datasheet
OK
Apply
Cancel