

8.1.4 PWR_BUCK_CTL

Buck Control Register

Address: 0x40280014

Retention: Retained

Protected Register Access: FUNCTION_POLICY

Bits	7	6	5	4	3	2	1	0
SW Access	None					RW		
HW Access	None					A		
Name	None [7:3]					BUCK_OUT1_SEL [2:0]		
Bits	15	14	13	12	11	10	9	8
SW Access	None							
HW Access	None							
Name	None [15:8]							
Bits	23	22	21	20	19	18	17	16
SW Access	None							
HW Access	None							
Name	None [23:16]							
Bits	31	30	29	28	27	26	25	24
SW Access	RW	RW	None					
HW Access	A	A	None					
Name	BUCK_OUT1_EN	BUCK_EN	None [29:24]					

Bits	Name	Description
31	BUCK_OUT1_EN	Enable for vodbuck1 output. The value in this register is ignored unless PWR_BUCK_CTL.BUCK_EN==1. This register is only reset by XRES/POR/BOD/HIBERNATE. The regulator takes up to 600us to charge the external capacitor. If there is additional load current while charging, this will increase the startup time. The TRM specifies the required sequence when transitioning vodb from the LDO to SIMO Buck output #1. Default Value: 0
30	BUCK_EN	Master enable for buck converter. This register is only reset by XRES/POR/BOD/HIBERNATE. Default Value: 0

The first-time power is applied to the P64 how do we get the buck regulator to output so we can get the device operational?