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// Generated Sequence for TLE9255 at 25.05.2023 09:40:06 W. Europe Standard Time

// output of the read(byte) function - exemplarily shown by checking the current mode of the
transceiver.
byte MODE_CTRL_VAL = read(0b00000001); // Reading value of MODE_CTRL.

// ***** SPI commands for configuring the TLE9255 *****

// Assumption: CAN Transceiver to be set in Normal Mode.

// Entering Normal Mode
write(0b10000001, 0b00001000); // Setting value 0x08 for MODE_CTRL.
// Hex Value: 0x08 | Byte Value: 0b00001000 | Register: MODE_CTRL

// Start to configure Baudrate, bit rate ratio and selective wake.

// Selected Baudrate: 500 kbps

// Selected BR Ratio: 4

// Selective Wake Unit: Enabled

write(0b10000110, 0b10000100); // Setting value 0x84 for SWK_CTRL_2.
// Hex Value: 0x84 | Byte Value: 10000100 | Register: SWK_CTRL_2

// Baudrate, bit rate ratio and selective wake successfully configured.

// Set ID and extended ID
write(0b10000111, 0b00011111); // Setting value 0x1f for SWK_ID3_CTRL.
// Hex Value: 0x1f | Byte Value: 0b00011111 | Register: SWK_ID3_CTRL

write(0b10001000, 0b11111100); // Setting value 0xfc for SWK_ID2_CTRL.
// Hex Value: 0xfc | Byte Value: 0b11111100 | Register: SWK_ID2_CTRL

write(0b10001001, 0b00000000); // Setting value 0x00 for SWK_ID1_CTRL.
// Hex Value: 0x00 | Byte Value: 0b00000000 | Register: SWK_ID1_CTRL

write(0b10001010, 0b00000000); // Setting value 0x00 for SWK_ID0_CTRL.
// Hex Value: 0x00 | Byte Value: 0b00000000 | Register: SWK_ID0_CTRL

// ID successfully set.

// Set Mask
write(0b10001011, 0b00011111); // Setting value 0x1f for SWK_MASK_ID3_CTRL.
// Hex Value: 0x1f | Byte Value: 0b00011111 | Register: SWK_MASK_ID3_CTRL

write(0b10001100, 0b11111100); // Setting value 0xfc for SWK_MASK_ID2_CTRL.
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// Hex Value: 0xfc | Byte Value: 0b11111100 | Register: SWK_MASK_ID2_CTRL

write(0b10001101, 0b00000000); // Setting value 0x00 for SWK_MASK_ID1_CTRL.
// Hex Value: 0x00 | Byte Value: 0b00000000 | Register: SWK_MASK_ID1_CTRL

write(0b10001110, 0b00000000); // Setting value 0x00 for SWK_MASK_ID0_CTRL.
// Hex Value: 0x00 | Byte Value: 0b00000000 | Register: SWK_MASK_ID0_CTRL

// Mask successfully set.

// Set DLC
write(0b10001111, 0b00000010); // Setting value 0x02 for SWK_DLC_CTRL.
// Hex Value: 0x02 | Byte Value: 0b00000010 | Register: SWK_DLC_CTRL

// DLC successfully set.

// Set payload
write(0b10010000, 0b00000000); // Setting value 0x00 for SWK_DATA7_CTRL. (Additional
information: This is Byte[7] of payload)
// Hex Value: 0x00 | Byte Value: 0b00000000 | Register: SWK_DATA7_CTRL

write(0b10010001, 0b00000000); // Setting value 0x00 for SWK_DATA6_CTRL. (Additional
information: This is Byte[6] of payload)
// Hex Value: 0x00 | Byte Value: 0b00000000 | Register: SWK_DATA6_CTRL

write(0b10010010, 0b00000000); // Setting value 0x00 for SWK_DATA5_CTRL. (Additional
information: This is Byte[5] of payload)
// Hex Value: 0x00 | Byte Value: 0b00000000 | Register: SWK_DATA5_CTRL

write(0b10010011, 0b00000000); // Setting value 0x00 for SWK_DATA4_CTRL. (Additional
information: This is Byte[4] of payload)
// Hex Value: 0x00 | Byte Value: 0b00000000 | Register: SWK_DATA4_CTRL

write(0b10010100, 0b00000000); // Setting value 0x00 for SWK_DATA3_CTRL. (Additional
information: This is Byte[3] of payload)
// Hex Value: 0x00 | Byte Value: 0b00000000 | Register: SWK_DATA3_CTRL

write(0b10010101, 0b00000000); // Setting value 0x00 for SWK_DATA2_CTRL. (Additional
information: This is Byte[2] of payload)
// Hex Value: 0x00 | Byte Value: 0b00000000 | Register: SWK_DATA2_CTRL

write(0b10010110, 0b11111111); // Setting value 0xff for SWK_DATA1_CTRL. (Additional
information: This is Byte[1] of payload)
// Hex Value: 0xff | Byte Value: 0b11111111 | Register: SWK_DATA1_CTRL
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write(0b10010111, 0b11111111); // Setting value 0xff for SWK_DATA0_CTRL. (Additional
information: This is Byte[0] of payload)
// Hex Value: 0xff | Byte Value: 0b11111111 | Register: SWK_DATA0_CTRL
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// Payload successfully set.
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write(0b10000101, 00000001); // Setting value 0x01 for SWK_CTRL_1.
// Hex Value: 0x01 | Byte Value: 10000001 | Register: SWK_CTRL_1
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// selective wakeup control set.
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//If you want to wake up from Sleep mode, then send the device to Sleep mode
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// Entering Sleep Mode
write(0b10000001, 0b00001001); // Setting value 0x01 for MODE_CTRL.
// Hex Value: 0x01 | Byte Value: 0b00000001 | Register: MODE_CTRL
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