PSoC6 Family of Devices

PSoC 61 Line
Ultra-Low-Power and High-Performance MCU Series

Device	Cores	Memory	Features
CY8C61xA	Arm Cortex-M4	2MB/1MB[1]	DAC[2], QSPI[3], FS-USB[4], SDHC[5], DC-DC
CY8C61x8	Arm Cortex-M4	1MB/512KB	DAC, QSPI, FS-USB, SDHC, DC-DC
CY8C61x7	Arm Cortex-M4	1MB/288KB	DAC, QSPI, UDB, FS-USB, DC-DC
CY8C61x6	Arm Cortex-M4	512KB/128KB	DAC, QSPI, UDB, FS-USB, DC-DC
CY8C61x5	Arm Cortex-M4	512KB/256KB	QSPI, UDB, FS-USB, CAN FD[7], SDHC
CY8C61x4	Arm Cortex-M4	256KB/128KB	QSPI, FS-USB, CAN FD, 2x ADC

PSoC 62 Line
Ultra-Low-Power, Dual-Core, and

Device	Cores	Memory	Features
CY8C62xA	Arm Cortex-M4 & Arm Cortex-M0+	2MB/1MB	DAC, QSPI, FS-USB, SDHC, DC-DC
CY8C62x8	Arm Cortex-M4 & Arm Cortex-M0+	1MB/512KB	DAC, QSPI, FS-USB, SDHC, DC-DC
CY8C62x7	Arm Cortex-M4 & Arm Cortex-M0+	1MB/288KB	DAC, QSPI, UDB, FS-USB, DC-DC
CY8C62x6	Arm Cortex-M4 & Arm Cortex-M0+	512KB/128KB	DAC, QSPI, UDB, FS-USB, DC-DC
CY8C62x5	Arm Cortex-M4 & Arm Cortex-M0+	512KB/256KB	QSPI, FS-USB, CAN FD, DC-DC, SDHC
CY8C62x4	Arm Cortex-M4 & Arm Cortex-M0+	256KB/128KB	QSPI, FS-USB, CAN FD, 2x ADC

PSoC 63 Line
High-Integration Wired/Wireless Connectivity MCU Series

Device	Cores	Memory	Features
CY8C63x7	Arm Cortex-M4 & Arm Cortex-M0+	1MB/288KB	DAC, QSPI, UDB, BLE, DC-DC
CY8C63x6	Arm Cortex-M4 & Arm Cortex-M0+	512KB/128KB, 1.7V-3.6V	DAC, QSPI, UDB, BLE, DC-DC

PSoC 64 Line
Ultra-Low-Power, Dual-Core, "Just Works" Secure Host MCU Series

Device	Cores	Memory	Features
CYB064xA	Arm Cortex-M4 & Arm Cortex-M0+	2MB/1MB	Secure-Boot MCU, Secure Flashboot, CY Secure Bootloader, MbedOS, AFR, IRTOS Support
CYB064x7	Arm Cortex-M4 & Arm Cortex-M0+	1MB/288KB	Secure Flashboot, CY Secure Bootloader, MbedOS, AFR, IRTOS Support
CYB064x5	Arm Cortex-M4 & Arm Cortex-M0+	512KB/256KB	Secure Flashboot, CY Secure Bootloader, MbedOS, AFR, IRTOS Support

PSoC 64 Line (continued)

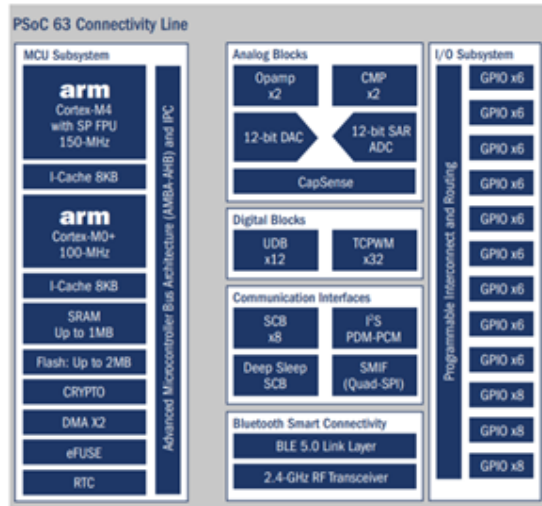| Device	Cores	Memory	Features
CYS0C64xA	Arm Cortex-M4 & Arm Cortex-M0+	2MB/1MB	AWS Standard Secure MCU, ARM_v7-M TF-M w/ PSA API, TF-M Integrated with AFR
CYB064x7-BL	Arm Cortex-M4 & Arm Cortex-M0+	1MB/288KB, BLE	Secure Flashboot, CY Secure Bootloader, MbedOS, AFR, IRTOS Support

PSoC4 Family of Devices

PSoC MCU PSoC 4000	Intelligent Analog PSoC 4100		Programmable Digital PSoC 4200	Analog Coprocessor PSoC 4A00		Application Specific PSoC 4500 , 4700
	CY8C4129-S 24-MHz M0+, 384K/32K[1] CMP[2], Opamp, ADC[3] SCB[4], MSC[12], Smart I/O[6]	CY8C4149-S 48-MHz M0+, 384K/32K CMP, Opamp, ADC SCB, MSC, Smart I/O				CY8C45xx-H **Motor Control** 150-MHz M4, 256K/128K CMP, Opamp 2X ADC, SCB, IDAC Smart I/O, ECO
	CY8C4128-S 24-MHz M0+, 256K/32K[1] CMP[2], Opamp, ADC[3] SCB[4], IDAC[5], Smart I/O[6]	CY8C4148-S 48-MHz M0+, 256K/32K CMP, Opamp, ADC SCB, IDAC, Smart I/O				
	CY8C4147-S 48-MHz M0+,128K/16K CMP, Opamp, ADC SCB, IDAC, Smart I/O	CY8C4128-BL 24-MHz M0, 256K/32K CMP, Opamp, ADC, SCB IDAC, BLE[7]	CY8C4247-M 48-MHz M0, 128K/16K, CMP, Opamp, ADC, SCB IDAC, UDB[8], CAN[9]			CY8C45xx-S **Motor Control** 48-MHz M0+, 256K/32K MCA[11], CMP, Opamp 2X ADC, SCB, IDAC Smart I/O, ECO
	CY8C4127-S 24-MHz M0+, 128K/16K CMP, Opamp, ADC SCB, IDAC, Smart I/O	CY8C4127-BL 24-MHz M0, 128K/16K CMP, Opamp, ADC, SCB IDAC, BLE	CY8C4246-M 48-MHz M0, 64K/8K, CMP, Opamp, ADC, SCB IDAC, UDB	CY8C4248-BL 48-MHz M0, 256K/32K CMP, Opamp, ADC, SCB IDAC, BLE, UDB	CY8C4Axx 48-MHz M0+, 32K/4K CMP, Opamp, UAB[10] ADC, SCB, VDAC Smart I/O	
CY8C40x-S 48-MHz M0+, 64K/8K CMP, ADC, SCB, MSC[12], Smart I/O	CY8C4127-M 24-MHz M0, 128K/16K CMP, Opamp, ADC, SCB IDAC	CY8C4146-S 48-MHz M0+, 64K/8K CMP, Opamp, ADC, SCB IDAC, Smart I/O	CY8C4246-DS 48-MHz M0, 64K/8K CMP, SCB UDB, Smart I/O	CY8C4247-BL 48-MHz M0, 128K/16K CMP, Opamp, ADC, SCB IDAC, BLE, UDB		CY8C47xx-S **Inductive Sensing** 48-MHz M0+, 32K/4K CMP, Opamp, UAB[10] ADC, SCB, VDAC Smart I/O
CY8C4045-S 48-MHz M0+, 32K/4K CMP, ADC, SCB IDAC, Smart I/O	CY8C4126-M 24-MHz M0, 64K/8K CMP, Opamp, ADC, SCB IDAC	CY8C41xx-PS 48-MHz M0+, 32K/4K CMP, Opamp, ADC SCB, VDAC, Smart I/O	CY8C4245-DS 48-MHz M0, 32K/4K CMP, SCB UDB, Smart I/O	CY8C4248-L 48-MHz M0, 256K/32K CMP, Opamp, ADC, SCB IDAC, UDB, CAN, USB		
CY8C4024-S 24-MHz M0+, 16K/2K CMP, ADC, SCB IDAC, Smart I/O	CY8C4125 24-MHz M0, 32K/4K CMP, Opamp, ADC, SCB IDAC	CY8C4125-S 24-MHz M0+, 32K/4K CMP, Opamp, ADC, SCB IDAC, Smart I/O	CY8C4245 48-MHz M0, 32K/4K CMP, Opamp, ADC, SCB IDAC, UDB	CY8C4247-L 48-MHz M0, 128K/16K CMP, Opamp, ADC, SCB IDAC, UDB, CAN, USB		
CY8C4014 16-MHz M0, 16K/2K CMP, I^{2}C, IDAC	CY8C4124 24-MHz M0, 16K/4K CMP, Opamp, ADC, SCB IDAC	CY8C4124-S 24-MHz M0+, 16K/4K CMP, Opamp, ADC, SCB IDAC, Smart I/O	CY8C4244 48-MHz M0, 16K/4K, CMP, Opamp, ADC, SCB IDAC, UDB	CY8C4246-L 48-MHz M0, 64K/8K CMP, Opamp, ADC, SCB IDAC, UDB, CAN, USB		

- For further insights you should refer to the datasheet s of each of the PSoC Family after having an overall idea about them through the above charts.
- The datasheets are available on our website – www.infineon.com under documents section of respective PSoC families.

Here is a brief difference in these two families, you might refer it for a rough idea aligning with our application requirements. Then dive into the datasheets and you should be able to figure out the specifics.
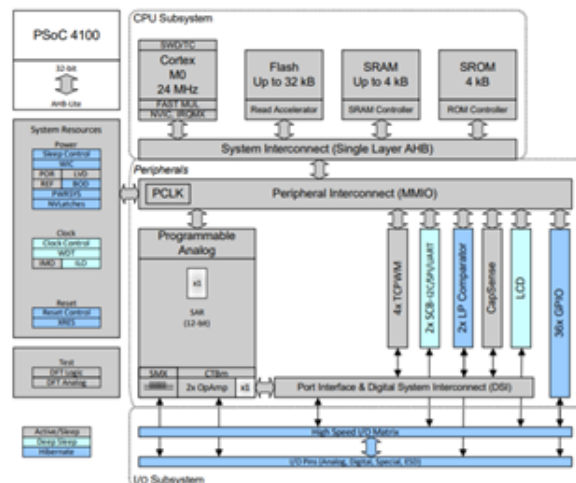
PSoC 6 Product Family

› Single or Dual Arm® Cortex®-M4 and Arm® Cortex®-M0+ CPUs
› Bluetooth Low Energy (Bluetooth Smart)
› Low-Power 1.7-V to 3.6-V Operation
› Programmable Analog
› Digital Blocks
› Communication Interfaces
› CapSense®
› Security

PSoC 4 Product Family

› Programmable Analog Blocks
› Programmable Digital Blocks
› Wired and Wireless Connectivity
› Capacitive Sensing
› Inductive sensing
› Motor Control

Note:- These architectural diagrams are there in our website, incase you want them.