Configure

GPIF mode and FlowState.

Chip: CY7C68013A

The operating speed of the chip is High-speed.

Endpoint 6 is DIR = IN, TYPE = BULK, SIZE = 512, BUF = 2x512. GPIF uses FF flag.

Endpoint 6: AUTOIN and WORDWIDE is 16 bit.

Set the Endpoint 6 AUTOIN packet length to 512 bytes.

GPIF Transaction Count is 256.

FlowState conditions shown in Figure 1.

IF (FIFO)Flag <u>▼</u> =1 AND	▼ FIFOFlag ▼ =1
THEN		T.
	✓ WEN# ✓ BEN#	☐ Enable output ☐ Enable output
	☑ OE#	Enable output
	▽ unused	
ELSE	<u></u>	
	✓ WEN#	☐ Enable output
	REN#	☐ Enable output ☐ Enable output
	✓ unused	

Figure 1

The ChipScope wareform shown in Figure 2.

If I use the Endpoint 8 take the place of the Endpoint 6 to do the same experiment, it will occurs the same problem.

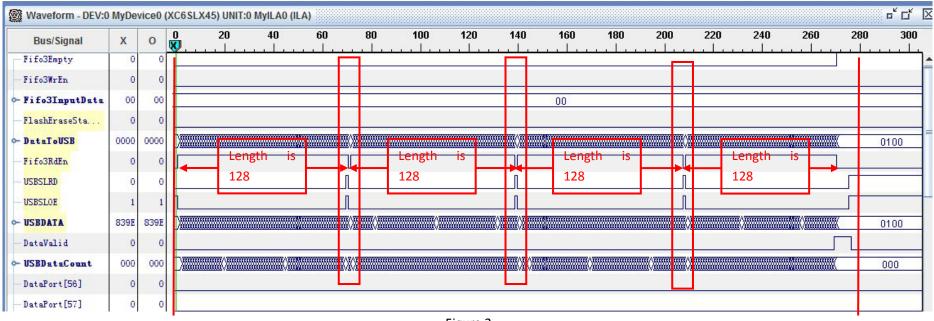


Figure 2

If Endpoint 6 is DIR = IN, TYPE = BULK, SIZE = 512, BUF = 4x512, others has not change. Then the ChipScope wareform shown in Figure 3.

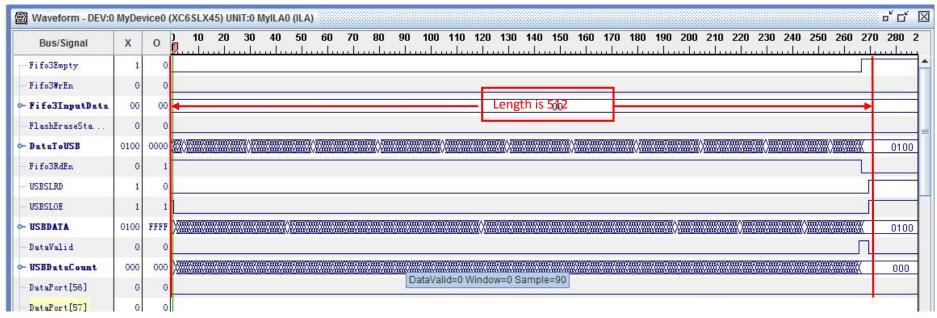


Figure 3

why?

I need the wareform like Figure 3 when EP6 is DIR = IN, TYPE = BULK, SIZE = 512, BUF = 2x512.

Thanks!