

Output control



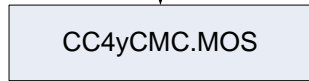
Level Active Selection



External count direction

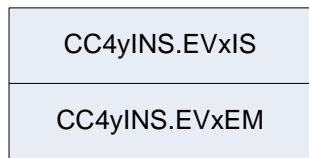
CDIR = 0/1

Up or Down count

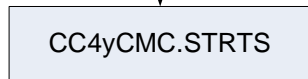


External Modulation

CC4yST = 0



Edge Active Selection



External Start / Stop

TRB = 0/1

Start or Stop timer.



External Reload

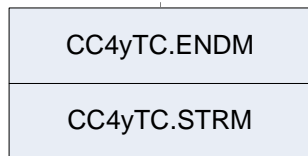
Load

Reload value from PR or CR.



Status bit overwrite

CC4yST = 0/1

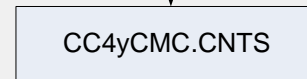


Input Capture



External Gating signal

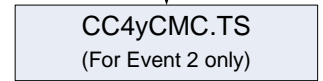
Pulse width measure



External Count Signal

Pulse counting

Special



TRAP function

TRAP