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##########
# \file Makefile
# \version 1.0
# \brief
# Top-level application make file.
#########
# \copvright
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#########
#########
# Basic Configuration
#########
# Type of ModusToolbox Makefile Options include:
# COMBINED
           -- Top Level Makefile usually for single standalone
application
# APPLICATION -- Top Level Makefile usually for multi project
application
# PROJECT
           -- Project Makefile under Application
MTB_TYPE=COMBINED
# Target board/hardware (BSP).
# To change the target, it is recommended to use the Library manager
# ('make library-manager' from command line), which will also update
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Eclipse IDE launch
# configurations.
TARGET=APP_CY8CKIT-062-WIFI-BT
# Name of application (used to derive name of final linked file).
# If APPNAME is edited, ensure to update or regenerate launch
# configurations for your IDE.
APPNAME=OurProj3
# Name of toolchain to use. Options include:
# GCC_ARM -- GCC provided with ModusToolbox software
         -- ARM Compiler (must be installed separately)
# ARM
# IAR
         -- IAR Compiler (must be installed separately)
# See also: CY COMPILER PATH below
TOOLCHAIN=GCC_ARM
# Default build configuration. Options include:
# Debug -- build with minimal optimizations, focus on debugging.
# Release -- build with full optimizations
# Custom -- build with custom configuration, set the optimization flag
in CFLAGS
# If CONFIG is manually edited, ensure to update or regenerate launch
configurations
# for your IDE.
CONFIG=Custom
# If set to "true" or "1", display full command—lines when building.
VERBOSE=
#########
# Advanced Configuration
#########
# Enable optional code that is ordinarily disabled by default.
# Available components depend on the specific targeted hardware and
firmware
# in use. In general, if you have
#
    COMPONENTS=foo bar
# ... then code in directories named COMPONENT_foo and COMPONENT_bar
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will be
# added to the build
COMPONENTS=FREERTOS WICED_BLE LWIP MBEDTLS SECURE_SOCKETS
# Like COMPONENTS, but disable optional code that was enabled by
default.
DISABLE COMPONENTS=
# Choose the core where the application and Bluetooth LE Host stack
must run
# CM4 -- Application and AIROC BTSTACK(Host stack) run on CM4
# CMOP -- Application and AIROC BTSTACK(Host stack) run on CMOP
ifneq ($(TARGET), $(filter $(TARGET), APP_CYW920829M2EVB-01))
CORE=CM4
endif
# By default the build system automatically looks in the Makefile's
directory
# tree for source code and builds it. The SOURCES variable can be used
# manually add source code to the build process from a location not
searched
# by default, or otherwise not found by the build system.
SOURCES=
# Like SOURCES, but for include directories. Value should be paths to
# directories (without a leading -I).
INCLUDES=./configs ./configs/COMPONENT_$(CORE)
# Custom configuration of mbedtls library.
MBEDTLSFLAGS = MBEDTLS USER CONFIG FILE='"mbedtls user config.h"'
# Add additional defines to the build process (without a leading -D).
DEFINES=$(MBEDTLSFLAGS) CYBSP WIFI CAPABLE
CY RETARGET IO CONVERT_LF_TO_CRLF CY_RTOS_AWARE
DISABLE MBEDTLS ACCELERATION
#DEFINES+=COMPONENT 43907
DEFINES+=NDEBUG
#DEFINES+=DISABLE MBEDTLS ACCELERATION
#DEFINES+=MBEDTLS_USER_CONFIG_FILE='"mbedtls_user_config.h"'
CY IGNORE=
CY IGNORE+= $(SEARCH aws-iot-device-sdk-embedded-C)/libraries/
standard/coreHTTP
CY IGNORE+= libs/aws-iot-device-sdk-embedded-C/libraries/standard/
coreHTTP
# CY8CPROTO-062-4343W board shares the same GPIO for the user button
(USER BTN1)
# and the CYW4343W host wake up pin. Since this example uses the GPIO
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for
# interfacing with the user button, the SDIO interrupt to wake up the
# disabled by setting CY WIFI HOST WAKE SW FORCE to '0'.
DEFINES+=CY WIFI HOST WAKE SW FORCE=0
# Select softfp or hardfp floating point. Default is softfp.
VFP SELECT=
# Additional / custom C compiler flags.
# NOTE: Includes and defines should use the INCLUDES and DEFINES
variable
# above.
CFLAGS=-0s
# Additional / custom C++ compiler flags.
# NOTE: Includes and defines should use the INCLUDES and DEFINES
variable
# above.
CXXFLAGS=-0s
# Additional / custom assembler flags.
# NOTE: Includes and defines should use the INCLUDES and DEFINES
variable
# above.
ASFLAGS=
# Additional / custom linker flags.
LDFLAGS=
# Additional / custom libraries to link in to the application.
LDLIBS=
# Path to the linker script to use (if empty, use the default linker
script).
LINKER SCRIPT=
# Custom pre-build commands to run.
PREBUILD=
# Custom post-build commands to run.
POSTBUILD=
# To change the default policy
CY_SECURE_POLICY_NAME=policy_single_CM0_CM4_smif_swap
```

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#########
# Paths
#########
# Relative path to the project directory (default is the Makefile's
directory).
# This controls where automatic source code discovery looks for code.
CY_APP_PATH=
# Relative path to the shared repo location.
# All .mtb files have the format, <URI>#<COMMIT>#<LOCATION>. If the
<LOCATION> field
# begins with $$ASSET_REPO$$, then the repo is deposited in the path
specified by
# the CY_GETLIBS_SHARED_PATH variable. The default location is one
directory level
# above the current app directory.
# This is used with CY_GETLIBS_SHARED_NAME variable, which specifies
the directory name.
CY_GETLIBS_SHARED_PATH=../
# Directory name of the shared repo location.
CY GETLIBS SHARED NAME=mtb shared
# Absolute path to the compiler's "bin" directory.
#
# The default depends on the selected TOOLCHAIN (GCC ARM uses the
ModusToolbox
# software provided compiler by default).
CY COMPILER PATH=
# Locate ModusToolbox helper tools folders in default installation
# locations for Windows, Linux, and macOS.
CY_WIN_HOME=$(subst \,/,$(USERPROFILE))
CY TOOLS PATHS ?= $(wildcard \
    $(CY WIN HOME)/ModusToolbox/tools * \
    $(HOME)/ModusToolbox/tools * \
    /Applications/ModusToolbox/tools *)
# If you install ModusToolbox software in a custom location, add the
path to its
# "tools X.Y" folder (where X and Y are the version number of the
tools
# folder). Make sure you use forward slashes.
CY_T00LS_PATHS+=
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# Default to the newest installed tools folder, or the users override
(if it's
# found).
CY_TOOLS_DIR=$(lastword $(sort $(wildcard $(CY_TOOLS_PATHS))))

ifeq ($(CY_TOOLS_DIR),)
$(error Unable to find any of the available CY_TOOLS_PATHS -- $
(CY_TOOLS_PATHS). On Windows, use forward slashes.)
endif

$(info Tools Directory: $(CY_TOOLS_DIR))

include $(CY_TOOLS_DIR)/make/start.mk
```