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 */

/** @file
 * NVRAM variables taken from BCM943438WLPTH nvram file (2.4 GHz, 20 MHz BW mode)
 */

#ifndef INCLUDED_NVRAM_IMAGE_H_
#define INCLUDED_NVRAM_IMAGE_H_

#include <string.h>
#include <stdint.h>
#include "../generated_mac_address.txt"

#ifdef __cplusplus
extern "C" {
#endif

/**
 * Character array of NVRAM image

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*/

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static const char wifi_nvram_image[] =
    // # The following parameter values are just placeholders, need to be
    updated.
    "manfid=0x2d0"                "\x00"
    "prodid=0x0726"              "\x00"
    "vendid=0x14e4"              "\x00"
    "devid=0x43e2"               "\x00"
    "boardtype=0x0726"           "\x00"
    "boardrev=0x1101"            "\x00"
    "boardnum=22"                 "\x00"
    "xtalfreq=37400"              "\x00"
    "sromrev=11"                  "\x00"
    "boardflags=0x00404201"       "\x00"
    "boardflags3=0x08000000"      "\x00"
    NVRAM_GENERATED_MAC_ADDRESS   "\x00"
    "nocrc=1"                     "\x00"
    "ag0=255"                     "\x00"
    "aa2g=1"                       "\x00"
    "ccode=ALL"                    "\x00"
    "\x00"
    // #Antenna diversity
    "swdiv_en=1"                   "\x00"
    "swdiv_gpio=2"                 "\x00"

    "pa0itssit=0x20"              "\x00"
    "extpagain2g=0"                "\x00"
    // #PA parameters for 2.4GHz, measured at CHIP OUTPUT
    "pa2ga0=-140,6566,-728"        "\x00"
    "AvVmid_c0=0x0,0xc8"          "\x00"
    "cckpwroffset0=5"              "\x00"
    // # PPR params
    "maxp2ga0=84"                  "\x00"
    "txpwrbckof=6"                 "\x00"
    "cckbw202gpo=0"                "\x00"
    "legofdmbw202gpo=0x66111111"   "\x00"
    "mcsbw202gpo=0x77711111"       "\x00"
    "propbw202gpo=0xdd"            "\x00"
    // # OFDM IIR :
    "ofdmdigfilttype=18"           "\x00"
    "ofdmdigfilttypebe=18"         "\x00"
    // # PAPD mode:
    "papdmode=1"                    "\x00"
    "papdvalidtest=1"              "\x00"
    "pacalidx2g=32"                 "\x00"
    "papdepsoffset=-36"            "\x00"
    "papdendidx=61"                 "\x00"
    // # LTECX flags
    // "ltecxmux=1"
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"\x00"
    //"ltecxpadnum=0x02030401"
"\x00"
    // "ltecxfnsl=0x3003"
"\x00"
    // "ltecxgcigpio=0x3012"
"\x00"
    // #il0macaddr=00:90:4c:c5:12:38
    "wl0id=0x431b"
    "deadman_to=0xffffffff"
    // # muxenab: 0x1 for UART enable, 0x2 for GPIOs, 0x8 for JTAG, 0x10 for HW
OOB
    "muxenab=0x11"
    // # CLDO PWM voltage settings - 0x4 - 1.1 volt
    // # cldo_pwm=0x4
    // # VCO freq 326.4MHz
    "spurconfig=0x3"
    "\x00\x00";

#ifdef __cplusplus
} /* extern "C" */
#endif

#else /* ifndef INCLUDED_NVRAM_IMAGE_H_ */

#error Wi-Fi NVRAM image included twice

#endif /* ifndef INCLUDED_NVRAM_IMAGE_H_ */

```