

Default

HX3 Blaster Plus

Select a HX3 device:  Always display settings from EEPROM

Configuration type: Configuration D : Configuration C + String descriptors

General settings: VID: i04B4, USB 3.0 PID: e5504, USB 2.0 PID: e5506, DID: e5500, Embedded Hub: i32, Power on time: i8, OC timer: i8, Accept illegal descriptor: b1, Compound device: b0, SS US port disable: b0

GPIO Control: Port indicators: b1, Disable SS LED: b0, Disable green LED: b0, Disable amber LED: b0, Suspend indicator disable: b0, LED modulate enable: b1

Port settings: SS Active: b1, Shared Link enable: b0, SS Tx amplitude (V): 1.0 V, SS Tx driver de-emphasis (db): -3.4 db, HS Active: b1, HS swap port polarity: b0, HS driver pre-emphasis enable: b0, HS driver pre-emphasis depth (%): +10%, HS amplitude boost (%): Default, Removable port: b1, Enable CDP: b1, Common HS PHY settings: HS driver slope (%): Default, Reference voltage for HS squelch (mV): 120 mV

Raw configuration data:

Offset	Byte
0x00	0x43
0x01	0x59
0x02	0x30
0x03	0x04
0x04	0xC0
0x05	0x84
0x06	0x04
0x07	0x04
0x08	0x65
0x09	0x00
0x0A	0x50
0x0B	0x00
0x0C	0x0F
0x0D	0x32
0x0E	0xFF
0x0F	0x10
0x10	0x00
0x11	0x11
0x12	0xF0
0x13	0x68
0x14	0x00
0x15	0x08
0x16	0x00
0x17	0x00
0x18	0x12

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Initialized Blaster Plus for use with 88 pin device without Shared Link. No. of devices connected: 0 | Package: NONE | HX3 F/W version: 0.0.0 | EEPROM Status: NOT CONNECTED

HX3 Blaster Plus

Select a HX3 device:  Always display settings from EEPROM

Configuration type: Configuration D : Configuration C + String descriptors

General settings: VID: i04B4, USB 3.0 PID: e5504, USB 2.0 PID: e5506, DID: e5500, Embedded Hub: i32, Power on time: i8, OC timer: i8, Accept illegal descriptor: b1, Compound device: b0, SS US port disable: b0

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Port settings: SS Active: b1, Shared Link enable: b0, SS Tx amplitude (V): 1.0 V, SS Tx driver de-emphasis (db): -3.4 db, HS Active: b0, HS swap port polarity: b0, HS driver pre-emphasis enable: b0, HS driver pre-emphasis depth (%): +10%, HS amplitude boost (%): Default, Removable port: b1, Enable CDP: b1, Common HS PHY settings: HS driver slope (%): Default, Reference voltage for HS squelch (mV): 120 mV

Raw configuration data:

Offset	Byte
0x12	0xF0
0x13	0x68
0x14	0x00
0x15	0x08
0x16	0x00
0x17	0x00
0x18	0x12
0x19	0x00
0x1A	0x2C
0x1B	0x65
0x1C	0x66
0x1D	0x69
0x1E	0x29
0x1F	0x29
0x20	0x29
0x21	0x29
0x22	0x00
0x23	0x06
0x24	0x65
0x25	0x00
0x26	0x00
0x27	0x00
0x28	0x00
0x29	0x00
0x2A	0x00
0x2B	0x00
0x2C	0x00
0x2D	0x03
0x2E	0x09
0x2F	0x04
0x30	0x12

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Initialized Blaster Plus for use with 88 pin device without Shared Link. No. of devices connected: 0 | Package: NONE | HX3 F/W version: 0.0.0 | EEPROM Status: NOT CONNECTED

Custom

HX3 Blaster Plus

Select a HX3 device:  Always display settings from EEPROM

Configuration type: Configuration D : Configuration C + String descriptors

General settings: VID: i04B4, USB 3.0 PID: e5508, USB 2.0 PID: e550A, DID: e5500, Embedded Hub: i32, Power on time: i8, OC timer: i8, Accept illegal descriptor: b1, Compound device: b0, SS US port disable: b0

GPIO Control: Port indicators: b1, Disable SS LED: b0, Disable green LED: b0, Disable amber LED: b0, Suspend indicator disable: b0, LED modulate enable: b1

Port settings: SS Active: b1, Shared Link enable: b0, SS Tx amplitude (V): 1.0 V, SS Tx driver de-emphasis (db): -3.4 db, HS Active: b1, HS swap port polarity: b0, HS driver pre-emphasis enable: b0, HS driver pre-emphasis depth (%): +10%, HS amplitude boost (%): Default, Removable port: b1, Enable CDP: b1, Common HS PHY settings: HS driver slope (%): Default, Reference voltage for HS squelch (mV): 120 mV

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Offset	Byte
0x00	0x43
0x01	0x59
0x02	0x30
0x03	0x04
0x04	0xC0
0x05	0x84
0x06	0x04
0x07	0x08
0x08	0x65
0x09	0x00
0x0A	0x50
0x0B	0x00
0x0C	0x0F
0x0D	0x32
0x0E	0xFF
0x0F	0x10
0x10	0x00
0x11	0x18
0x12	0xF0
0x13	0x68
0x14	0x00
0x15	0x08
0x16	0x00
0x17	0x00
0x18	0x12

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Selected configuration length is 0xC4. No. of devices connected: 0 | Package: NONE | HX3 F/W version: 0.0.0 | EEPROM Status: NOT CONNECTED

120 Speed is set to 400kHz

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Port settings: SS Active: b1, Shared Link enable: b0, SS Tx amplitude (V): 1.0 V, SS Tx driver de-emphasis (db): -3.4 db, HS Active: b0, HS swap port polarity: b0, HS driver pre-emphasis enable: b0, HS driver pre-emphasis depth (%): +10%, HS amplitude boost (%): Default, Removable port: b1, Enable CDP: b1, Common HS PHY settings: HS driver slope (%): Default, Reference voltage for HS squelch (mV): 120 mV

Raw configuration data:

Offset	Byte
0x18	0x12
0x19	0x00
0x1A	0x2C
0x1B	0x65
0x1C	0x66
0x1D	0x69
0x1E	0x29
0x1F	0x29
0x20	0x29
0x21	0x29
0x22	0x00
0x23	0x0A
0x24	0x65
0x25	0x01
0x26	0xC0
0x27	0x16
0x28	0x00
0x29	0x00
0x2A	0x00
0x2B	0x00
0x2C	0x00
0x2D	0x00
0x2E	0x03
0x2F	0x09
0x30	0x12

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