Log file for this session is located at: C:\Users\SelvarajSamp\AppData\Local\Temp\PSoC Creator-000.log

Unable to download latest online manifest: The underlying connection was closed: Could not establish trust relationship for the SSL/TLS secure channel.The following projects have new component updates available: PLC\_TxRx\_SEG\_2.3. To update the components in a project right-click on the project in the Workspace Explorer and select 'Update Components' or click on the notification icon in the status bar.

ADD: sdb.M0065: information: Analog terminal "SAR\_Spare.analog\_0" on TopDesign is unconnected.

\* C:\Users\SelvarajSamp\Documents\MyProjects\PLC-SEG\projects\2021\_08\_25\PLC\_TxRx\_SEG\_2.3.cyprj.Archive03\PLC\_TxRx\_SEG\_2.3.cydsn\TopDesign\TopDesign.cysch (Signal: Net\_1745)

\* C:\Users\SelvarajSamp\Documents\MyProjects\PLC-SEG\projects\2021\_08\_25\PLC\_TxRx\_SEG\_2.3.cyprj.Archive03\PLC\_TxRx\_SEG\_2.3.cydsn\TopDesign\TopDesign.cysch (Shape\_5380.3)

ADD: sdb.M0052: error: Unable to find component "cy\_boot\_v5\_80".

ADD: sdb.M0061: information: Info from component: Filter\_1. The SwitchedCapFilter\_v1\_10 component (Filter\_1) is not compatible with the selected device. Please check the component datasheet for details on updating to a compatible component.

\* C:\Users\SelvarajSamp\Documents\MyProjects\PLC-SEG\projects\2021\_08\_25\PLC\_TxRx\_SEG\_2.3.cyprj.Archive03\PLC\_TxRx\_SEG\_2.3.cydsn\TopDesign\TopDesign.cysch (Instance:Filter\_1)

ADD: sdb.M0061: information: Info from component: ADC\_1. The Scan\_ADC\_v3\_10 component (ADC\_1) is not compatible with the selected device. Please check the component datasheet for details on updating to a compatible component.

\* C:\Users\SelvarajSamp\Documents\MyProjects\PLC-SEG\projects\2021\_08\_25\PLC\_TxRx\_SEG\_2.3.cyprj.Archive03\PLC\_TxRx\_SEG\_2.3.cydsn\TopDesign\TopDesign.cysch (Instance:ADC\_1)

ADD: sdb.M0061: information: Info from component: ADC\_1. Off-chip Vref bypass does not have any effect when Vref is set to VDDA.

\* C:\Users\SelvarajSamp\Documents\MyProjects\PLC-SEG\projects\2021\_08\_25\PLC\_TxRx\_SEG\_2.3.cyprj.Archive03\PLC\_TxRx\_SEG\_2.3.cydsn\TopDesign\TopDesign.cysch (Instance:ADC\_1)

Pin Guidance 2 4.1339894s

DEL: sdb.M0052: error: Unable to find component "cy\_boot\_v5\_80".

Pin Guidance 3 1.3301423s

Pin Guidance 4 1.4044259s

DEL: sdb.M0065: information: Analog terminal "SAR\_Spare.analog\_0" on TopDesign is unconnected.

\* C:\Users\SelvarajSamp\Documents\MyProjects\PLC-SEG\projects\2021\_08\_25\PLC\_TxRx\_SEG\_2.3.cyprj.Archive03\PLC\_TxRx\_SEG\_2.3.cydsn\TopDesign\TopDesign.cysch (Signal: Net\_1745)

\* C:\Users\SelvarajSamp\Documents\MyProjects\PLC-SEG\projects\2021\_08\_25\PLC\_TxRx\_SEG\_2.3.cyprj.Archive03\PLC\_TxRx\_SEG\_2.3.cydsn\TopDesign\TopDesign.cysch (Shape\_5380.3)

DEL: sdb.M0061: information: Info from component: ADC\_1. Off-chip Vref bypass does not have any effect when Vref is set to VDDA.

\* C:\Users\SelvarajSamp\Documents\MyProjects\PLC-SEG\projects\2021\_08\_25\PLC\_TxRx\_SEG\_2.3.cyprj.Archive03\PLC\_TxRx\_SEG\_2.3.cydsn\TopDesign\TopDesign.cysch (Instance:ADC\_1)

ADD: sdb.M0065: information: Analog terminal "SAR\_Spare.analog\_0" on TopDesign is unconnected.

\* C:\Users\SelvarajSamp\Documents\MyProjects\PLC-SEG\projects\2021\_08\_25\PLC\_TxRx\_SEG\_2.3.cyprj.Archive03\PLC\_TxRx\_SEG\_2.3.cydsn\TopDesign\TopDesign.cysch (Signal: Net\_1745)

\* C:\Users\SelvarajSamp\Documents\MyProjects\PLC-SEG\projects\2021\_08\_25\PLC\_TxRx\_SEG\_2.3.cyprj.Archive03\PLC\_TxRx\_SEG\_2.3.cydsn\TopDesign\TopDesign.cysch (Shape\_5380.3)

ADD: sdb.M0061: information: Info from component: ADC\_1. Off-chip Vref bypass does not have any effect when Vref is set to VDDA.

\* C:\Users\SelvarajSamp\Documents\MyProjects\PLC-SEG\projects\2021\_08\_25\PLC\_TxRx\_SEG\_2.3.cyprj.Archive03\PLC\_TxRx\_SEG\_2.3.cydsn\TopDesign\TopDesign.cysch (Instance:ADC\_1)

----------------Components are updated successfully---------------

System.NullReferenceException: Object reference not set to an instance of an object.

at CyDesigner.Device.FitterPSoC.M0S8.Mapper.CyM0S8MapInternal.CreateGlobalClockCell()

at CyDesigner.Device.FitterPSoC.Common.Mapper.CyPkMapInternal.Init()

at CyDesigner.Device.FitterPSoC.CyP3Fit.MapDesign()

at CyDesigner.Device.FitterPSoC.CyP3Fit.RunFitterPhases(IList`1 phases)

at CyDesigner.Device.FitterPSoC.CyP3Fit.RunFitter(IList`1 phases)

at CyDesigner.Device.FitterPSoC.CyP3Fit.GetPinGuidance()

at CyDesigner.Device.FitterPSoC.CyP3Fit.RunPinGuidance(CyOptions opts, CyReportFile rptFile, CyPrjMgmtProjectPSoCExe topPrj, ICyDcCatalog devCat)

at CyDesigner.Device.FitterPSoC.CyDsFit.GetPinGuidance(CyFullDesign design, CyStsMsgNoticeSet noticeSet, Object agent, CyPinGuidanceCachedWarpData cachedWarpData, CyPinGuidanceCachedWarpData& newCachedWarpData)Pin Guidance 5 0.6635255s

DEL: sdb.M0061: information: Info from component: ADC\_1. Off-chip Vref bypass does not have any effect when Vref is set to VDDA.

\* C:\Users\SelvarajSamp\Documents\MyProjects\PLC-SEG\projects\2021\_08\_25\PLC\_TxRx\_SEG\_2.3.cyprj.Archive03\PLC\_TxRx\_SEG\_2.3.cydsn\TopDesign\TopDesign.cysch (Instance:ADC\_1)

Pin Guidance 6 1.7424921s

ADD: sdb.M0061: information: Info from component: ADC\_1. Off-chip Vref bypass does not have any effect when Vref is set to VDDA.

\* C:\Users\SelvarajSamp\Documents\MyProjects\PLC-SEG\projects\2021\_08\_25\PLC\_TxRx\_SEG\_2.3.cyprj.Archive03\PLC\_TxRx\_SEG\_2.3.cydsn\TopDesign\TopDesign.cysch (Instance:ADC\_1)

Pin Guidance 7 1.6203663s

Pin Guidance 8 1.2312271s

Pin Guidance 9 1.3679410s

Pin Guidance 10 1.4566703s

----------------Components are updated successfully---------------

Pin Guidance 11 1.4483575s

Pin Guidance 12 1.4130708s

Pin Guidance 13 1.5394707s

Pin Guidance 14 1.2682799s

Pin Guidance 15 1.2447392s

----------------Components are updated successfully---------------

Pin Guidance 16 1.2997511s

Pin Guidance 17 1.2859819s

Pin Guidance 18 1.3994737s

DEL: sdb.M0061: information: Info from component: Filter\_1. The SwitchedCapFilter\_v1\_10 component (Filter\_1) is not compatible with the selected device. Please check the component datasheet for details on updating to a compatible component.

\* C:\Users\SelvarajSamp\Documents\MyProjects\PLC-SEG\projects\2021\_08\_25\PLC\_TxRx\_SEG\_2.3.cyprj.Archive03\PLC\_TxRx\_SEG\_2.3.cydsn\TopDesign\TopDesign.cysch (Instance:Filter\_1)

DEL: sdb.M0061: information: Info from component: ADC\_1. The Scan\_ADC\_v3\_10 component (ADC\_1) is not compatible with the selected device. Please check the component datasheet for details on updating to a compatible component.

\* C:\Users\SelvarajSamp\Documents\MyProjects\PLC-SEG\projects\2021\_08\_25\PLC\_TxRx\_SEG\_2.3.cyprj.Archive03\PLC\_TxRx\_SEG\_2.3.cydsn\TopDesign\TopDesign.cysch (Instance:ADC\_1)