

Information note

INF224203

Dear

ADMINISTRATOR PCN
MACNICA
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With this Infineon Technologies AG Information Note we would like to inform you about the following

Datasheet updates to select 2-Mbit to 8-Mbit EXCELON™ F-RAM products



On 16 April 2020, Infineon acquired Cypress.
We are now in the process of merging and consolidating our tools and processes for PCN, Information Notes, Errata and Product Discontinuance.
For further details, please visit our website:

<https://www.infineon.com/cms/en/about-infineon/company/cypress-acquisition/>

Cypress Semiconductor Corporation – An Infineon Technologies AG company,
198 Champion Court San Jose, CA 95134. Tel: (408) 943-2600

► **Products affected**

Please refer to attached affected product list [63]

► **Detailed change information**

Subject This information note is to inform customers about datasheet updates to the Write protect (\overline{WP}) and Hardware reset (\overline{RESET}) pins on select 2-Mbit to 8-Mbit EXCELON™ F-RAM products. The update excludes internal weak pull up configuration on the \overline{WP} and \overline{RESET} pins; therefore, it recommends that these two pins be tied to V_{DD} if not used.

Reason The datasheets have been updated to match the device specifications and actual F-RAM performance.

Description	Description	
	<u>Old</u>	<u>New</u>
	<ul style="list-style-type: none"> ■ \overline{WP}: This pin has an internal weak pull up resistor which keeps this pin HIGH if left floating (not connected on the board). This pin can also be tied to V_{DD} if not used. 	<ul style="list-style-type: none"> ■ \overline{WP}: This pin must be tied to V_{DD} if not used.
	<ul style="list-style-type: none"> ■ \overline{RESET}: This pin has an internal weak pull up resistor which keeps this pin HIGH if left floating (not connected on the board). This pin can also be tied to V_{DD} if not used. 	<ul style="list-style-type: none"> ■ \overline{RESET}: This pin must be tied to V_{DD} if not used.
	<ul style="list-style-type: none"> ■ \overline{RESET} signal should never be tied low even if \overline{RESET} functionality is disabled since it will increase leakage current due to the internal weak pull-up. 	<ul style="list-style-type: none"> ■ Removed the sentence from the datasheet.

► **Product identification**

Infineon maintains traceability of product to wafer level, including wafer fabrication location, through the lot number marked on the package.

► **Impact of change**

There is no change to the product. Infineon recommends that customers take this opportunity to review these changes against current application notes, system design considerations and customer environment conditions to assess impact (if any) to their application.

► **Attachments**

Affected product list [63]

Supporting document

Datasheets

► **Intended start of delivery**

Effective immediately on release of this information note.

If you have any questions, please do not hesitate to contact your local sales office.