CY15B104Q-SXI Application Circuit

BANK _15 OPERATES VCCO_14 = +3.3V

+3.3V

+3.3V

+3.3V

0.1uF x2

10kx3

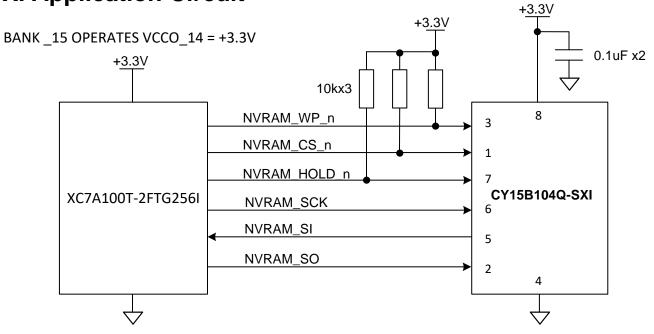| XC7A100T-2FTG256I		CY15B104Q-SXI

NVRAM_WP_n — 3

NVRAM_CS_n — 1

NVRAM_HOLD_n — 7

NVRAM_SCK — 6

NVRAM_SI — 5

NVRAM_SO — 2

8

4

XC7A100T-2FTG256I has PUDC_B=+3.3V so globally the XC7A100T has all I/O set to tristate (Hi-Z) during device configuration