

## 2nd Generation 3D Hall Sensors

ATV Sense & Control January 20



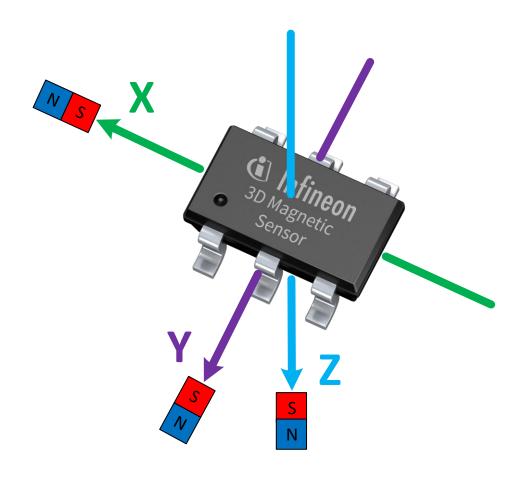




1 3D Hall sensor product concept 2
2 Portfolio and key features 7
3 Improvements of the 2nd generation 13







### 3DHall benefits



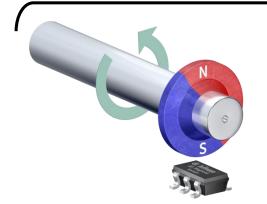


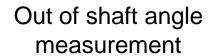


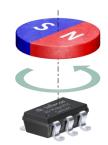


- ⇒ Strong flexibility in PCB orientation!
- ⇒ Very small 3 mm x 3 mm package!

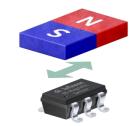
Supporting the applications







End of shaft angle measurement



Linear movement measurement

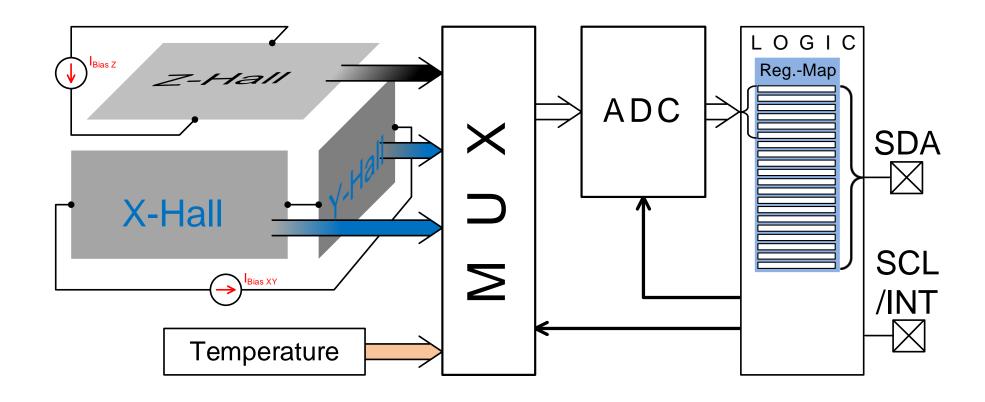


PE

Joystick movement measurement

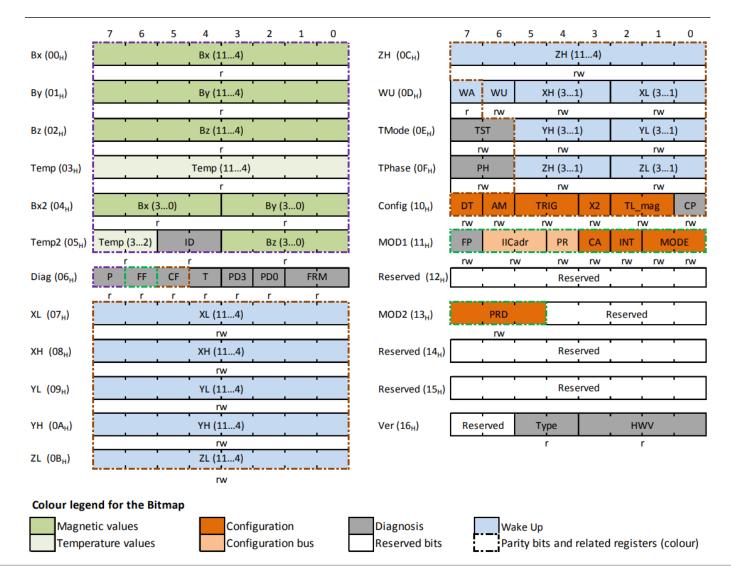














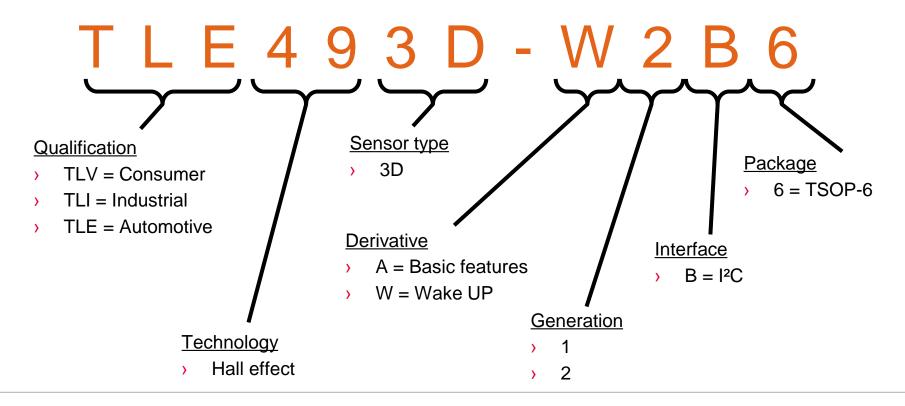


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	TLE493D-W2B6	TLE493D-A2B6	TLI493D-A2B6	TLV493D-A1B6
Generation	2nd		1st	
Temperature range	-40125°C -40105°C		-40125°C	
Linear magnetic field range	selectable: min. 100 and 160 mT			typ. 130 mT
Magnetic accuracy	high (min./max. spec.)			basic (typ. spec.)
Update rate XYZ	typ. 7.5 kHz		typ. 3.3 kHz	
Functional safety	ASIL B (ISO26262 ready)		-	
Wake up mode	Yes		-	
Preconfigured address variants	4 (A0 A3)			

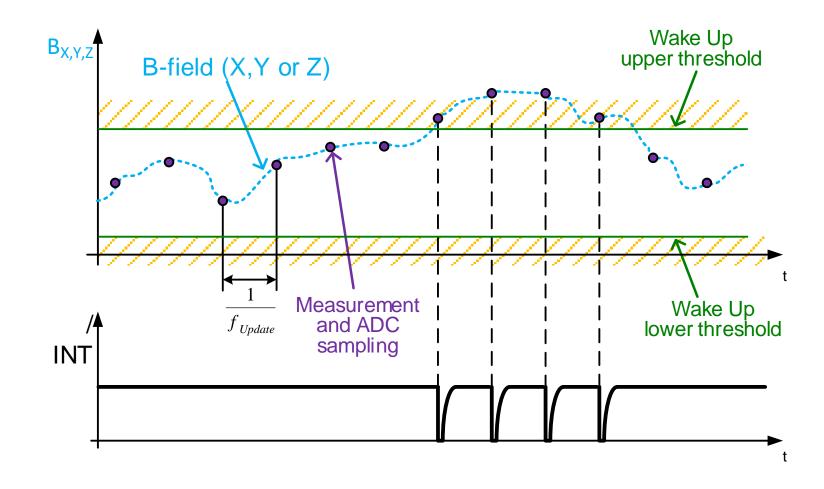
# Different power modes for a high flexibility (2nd generation 3D Hall)



Modes	Measurements	Typ. current consumption
Power down	No measurements:  ADC:	7 nA
Low power	Cyclic measurements with a selectable update rate between 0.05 and 700 Hz:  ADC:	0.3 μΑ430 μΑ
Fast	Continuous measurements:  ADC:	3.4 mA
Master controlled	Measurements triggered by the microcontroller via I <sup>2</sup> C:	Between power down and fast mode

# Power saving using the Wake-Up feature (TLE493D-W2B6)

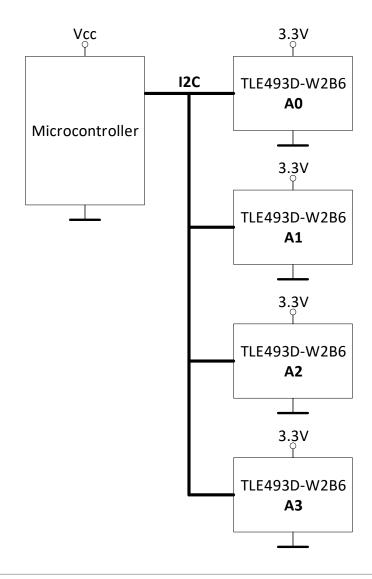






## Bus mode for multiple 3D Hall sensors

- 4 preconfigured address variants allow an out of the box bus mode
  - → TLE493D-W2B6 only
- > Alternative for all 3D Hall sensors:
  - Control the VDD of all sensors but the first with a GPIO pin
  - Reconfigure the I2C address at startup
- For functional safety critical applications a separate bus for each sensor is recommended







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## Improvements of the 2nd generation 3D Hall sensors

Topic	Description		
Higher reliability	<ul> <li>Designed to meet the automotive quality standards</li> <li>Min / max datasheet specifications</li> <li>Improved power on reset</li> <li>Improved ADC stability in fast and master controlled mode</li> <li>Removed dynamic I<sup>2</sup>C address setting at power-up</li> </ul>		
Higher performance	<ul> <li>Increased sensitivity (two magnetic field ranges)</li> <li>Higher update rate</li> <li>Generally higher accuracy</li> </ul>		
New features	<ul> <li>Improved master controlled mode:         <ul> <li>Flexible new ADC trigger options</li> <li>Automated power down after a finished measurement cycle</li> </ul> </li> <li>I<sup>2</sup>C clock stretching         <ul> <li>/INT collision avoidance</li> </ul> </li> <li>Advanced features with the TLE493D-W2B6 variant:         <ul> <li>Wake up mode</li> <li>Dedicated I<sup>2</sup>C address variants</li> <li>Functional safety support</li> <li>Additional low power modes</li> </ul> </li> </ul>		



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