

I find following error

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BDRV->CTRL1.bit.HS1\_DS\_STS=1;  
BDRV->CTRL1.bit.HS2\_DS\_STS=1;  
BDRV->CTRL1.bit.LS1\_DS\_STS=1;  
BDRV->CTRL1.bit.LS2\_DS\_STS=1;

BDRV->CTRL2.bit.HS3\_DS\_STS=1;  
BDRV->CTRL2.bit.LS3\_DS\_STS=1;

**short on external FET  
detected -short detected.**

I find the following error

BDRV->CTRL1.bit.HS1\_SUPPERR\_STS=1;  
BDRV->CTRL1.bit.HS2\_SUPPERR\_STS=1;  
BDRV->CTRL1.bit.LS1\_SUPPERR\_STS=1;  
BDRV->CTRL1.bit.LS2\_SUPPERR\_STS=1;

BDRV->CTRL1.bit.HS3\_SUPPERR\_STS=1;  
BDRV->CTRL1.bit.LS3\_SUPPERR\_STS=1;

**SUPPLY ERROR  
DETECTED**

I have tried to reset this bits as described in the data sheet. But still no able to do so.

Please provide your valuable suggestion to resolve these issue.

You can call me on 017627755601.

Thank you.

DA:TL987x Examples\BRDV\_15Nov\brdv15nov.uvprojx - uVision

File Edit View Project Flash Debug Peripherals Tools SVCS Window Help

Registers

Register	Value
R0	0x00000000
R1	0xE000ED20
R2	0x40028000
R3	0x18000068
R4	0x00000000
R5	0x18000008
R6	0x00000000
R7	0x00000000
R8	0x00000000
R9	0x00000000
R10	0x11000848
R11	0x00000000
R12	0x18000048
R13 (SP)	0x18000268
R14 (LR)	0x11000713
R15 (PC)	0x11000744
xPSR	0x01000000

Disassembly

```

0x1100079E 5000 DCW 0x5000
8: TLE_Init();
0x110007A0 F7FFFAC BL.W TLE_Init (0x110006FC)
9: CCU6_StartTmr_T12();
->0x110007A4 BF00 NOP
456: CCU6->ICTR4.reg = CCU6_MASK ICTR4_START_T12;

```

main.c

```

3
4
5 int main(void)
6 {
7
8 TLE_Init();
9 CCU6_StartTmr_T12();
10
11 // CCU6_StartTmr_T13();
12 // CCU6_LoadShadowRegister_CC60(10);
13 // CCU6_LoadShadowRegister_CC61(10);
14 // CCU6_LoadShadowRegister_CC62(10);
15
16 // BDRV->CTRL1.bit.LS1_DCS_EN=1;
17 // BDRV->CTRL1.bit.LS2_DCS_EN=1;
18 // BDRV->CTRL1.bit.HS1_DCS_EN=1;
19 // BDRV->CTRL1.bit.HS2_DCS_EN=1;
20
21 // BDRV->CTRL2.bit.LS3_DCS_EN=1;

```

Property Value

BDRV

- CP\_CLK\_CTRL: 0x0000CA16
- CP\_CTRL\_STS: 0x0C270021
- CTRL1: 0x29393939
  - HS2\_OC\_DIS: 0: ENABLE = enable overcurrent shutdown of driver
  - HS2\_OC\_STS: 0: no\_Overcurrent = no over-current Condition occurred.
  - HS2\_SUPERR\_STS: 1: SUPPLY\_ERROR = detected; this flag is an OR of the VDS\_x\_STS and VCP\_x\_STS flags.
  - HS2\_DS\_STS: 1: short\_on\_external\_FET\_detected = short detected; write clear status.
  - HS2\_DCS\_EN: 1: ENABLE = enable current source; short diagnosis can be performed by evaluating the LSx/HSx\_...
  - HS2\_ON: 0: OFF = Driver off
  - HS2\_PWM: 0: DISABLE = disables control by PWM input
  - HS2\_EN: 1: ENABLE = Driver circuit power on
- HS1\_OC\_DIS: 0: ENABLE = enable overcurrent shutdown of driver
- HS1\_OC\_STS: 0: no\_Overcurrent = no over-current Condition occurred.
- HS1\_SUPERR\_STS: 1: SUPPLY\_ERROR = detected; this flag is an OR of the VDS\_x\_STS and VCP\_x\_STS flags.
- HS1\_DS\_STS: 1: short\_on\_external\_FET\_detected = short detected; write clear status.
- HS1\_DCS\_EN: 1: ENABLE = enable current source; short diagnosis can be performed by evaluating the LSx/HSx\_...
- HS1\_ON: 0: OFF = Driver off
- HS1\_PWM: 0: DISABLE = disables control by PWM input
- HS1\_EN: 1: ENABLE = Driver circuit power on
- LS2\_OC\_DIS: 0: ENABLE = enable overcurrent shutdown of driver
- LS2\_OC\_STS: 0: no\_Overcurrent = no over-current Condition occurred.
- LS2\_SUPERR\_STS: 1: SUPPLY\_ERROR = detected; this flag is an OR of the VDS\_x\_STS and VCP\_x\_STS flags.
- LS2\_DS\_STS: 1: short\_on\_external\_FET\_detected = short detected; write clear status.

Command

```

*** error 35: undefined line number

```

Call Stack - Locals

main 0x0000... int f0

ASSIGN BreakDisable BreakEnable BreakKill

J-LINK / J-TRACE Cortex t1: 0.00000000 sec L9 C1 CAP\_NUM SCRL OVR RAW

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Disassembly

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0x1100079E 5000 DCW 0x5000
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18 // BDRV->CTRL1.bit.HS1_DCS_EN=1;
19 // BDRV->CTRL1.bit.HS2_DCS_EN=1;
20
21 // BDRV->CTRL2.bit.LS3_DCS_EN=1;

```

Property Value

BDRV

- LS2\_DS\_STS: 1: short\_on\_external\_FET\_detected = short detected; write clear status.
- LS2\_DCS\_EN: 1: ENABLE = enable current source; short diagnosis can be performed by evaluating the LSx/HSx\_...
- LS2\_ON: 0: OFF = Driver off
- LS2\_PWM: 0: DISABLE = disables control by PWM input
- LS2\_EN: 1: ENABLE = Driver circuit power on
- LS1\_OC\_DIS: 0: ENABLE = enable overcurrent shutdown of driver
- LS1\_OC\_STS: 0: no\_Overcurrent = no over-current Condition occurred.
- LS1\_SUPERR\_STS: 1: SUPPLY\_ERROR = detected; this flag is an OR of the VDS\_x\_STS and VCP\_x\_STS flags.
- LS1\_DS\_STS: 1: short\_on\_external\_FET\_detected = short detected; write clear status.
- LS1\_DCS\_EN: 1: ENABLE = enable current source; short diagnosis can be performed by evaluating the LSx/HSx\_...
- LS1\_ON: 0: OFF = Driver off
- LS1\_PWM: 0: DISABLE = disables control by PWM input
- LS1\_EN: 1: ENABLE = Driver circuit power on
- CTRL2: 0x00003939
  - DLY\_DIAG\_DIRS...: 0: TURN\_OFF = measure turn on time
  - DLY\_DIAG\_CHSEL: 0: DISABLE = diag timer deactivated.
  - DLY\_DIAG\_STS: 0: Diag\_timer\_invalid = diag timer measurement ongoing
  - DLY\_DIAG\_SCLR: 0: Diag\_timer\_valid\_not\_clear = .
  - DLY\_DIAG\_TIM: 0x0000
  - HS3\_OC\_DIS: 0: ENABLE = enable overcurrent shutdown of driver.
  - HS3\_OC\_STS: 0: no\_Overcurrent = no over-current Condition occurred.
  - HS3\_SUPERR\_STS: 1: SUPPLY\_ERROR = detected; this flag is an OR of the VDS\_x\_STS and VCP\_x\_STS flags.
  - HS3\_DS\_STS: 1: short\_on\_external\_FET\_detected = short detected; write clear status.

Command

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*** error 35: undefined line number

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Call Stack - Locals

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