

Patch code starts at 0x270400 (RAM address)  
Patch code ends at 0x277A04 (RAM address)  
Patch ram starts at 0x2129AC (RAM address)  
Patch ram ends at 0x212E9C (RAM address)

Application memory usage:

.text SRAM start 0x212E9C, end 0x21302C, size 400  
.rodata SRAM start 0x21302C, end 0x2131E9, size 445  
.data SRAM start 0x2131EC, end 0x213213, size 39  
.bss SRAM start 0x213214, end 0x213216, size 2  
.setup SRAM start 0x213218, end 0x213298, size 128  
Battery backed RAM start 0x2837C0, end 0x2837C0, size 0  
Static Data start 0x500C00, end 0x500C00, size 0  
ram (SRAM): used 0x212E9C - 0x213298 size (1020)  
Total application footprint 1020 (0x3FC)

DS available 520192 (0x07F000) at 0x503000